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...circumvents both the noise-susceptibility and the increased-power problems of existing multi-state data quadrature amplitude (m-QAM) modulations by using mutually orthogonal states for each bit transmitted. In addition, each pulse has two distinct quadrature...  
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**DRESS, William, B., Jr. / SMITH, Stephen F., EUROPEAN PATENT**, Aug 2002  
The invention relates generally to the field of pulse transmission communications. More particularly, the invention relates to pulse transmission, spread-spectrum modes of low-power radio communications. Prior art time-domain communications techniques are known to those skilled in the art. The  
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A method and apparatus for carrying out synchronous co- division multiple access (SCDMA) communication of multiple channels of digital data over a shared transmission media (1162). The system includes modems at remote units (1164) and a central unit ...  
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A system for bidirectional communication of digital data between a central unit and a remote unit wherein the need for tracking loops in the central unit has been eliminated. The central unit transmitter generates a master carrier and a master clock ...  
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**Rakib, Selim Shlomo / Azenkot, Yehuda, EUROPEAN PATENT**, Jun 2001  
This application is a divisional application from the applicant's earlier European patent application number 96927270.7, granted as EP 0 858 695 B1, and also relates to the applicant's granted U. S. patents 5,793,759, issued 11 August 1998, and 5,768,269, issued 16 June 1998, which existed as U.S.  
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**Dress, William B. / Smith, Stephen F., UNITED STATES PATENT AND TRADEMARK**

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...circumvents both the noise-susceptibility and the increased-power problems of existing multi-state data quadrature amplitude (m-QAM) modulations by using mutually orthogonal states for each bit transmitted. In addition, each pulse has two distinct quadrature...

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**DRESS, William, B., Jr. / SMITH, Stephen F., PATENT COOPERATION TREATY APPLICATION**, Mar 2001

Systems and methods for pulse- transmission low-power communication modes are disclosed. A method of pulse transmission communications Includes: generating a modulated pulse signal waveform; transforming said modulated pulse signal waveform into at least ...

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orthogonal states for each bit transmitted. In addition, each pulse has two distinct quadrature...

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**Dress, William B. / Smith, Stephen F., UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION**, Jun 2002


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☐ **17. [Apparatus and method for trellis encoding data for transmission in digital data transmission systems](#)**

**Rakib, Selim Shlomo / Azenkot, Yehuda, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION**, Feb 2002

A system for bidirectional communication of digital data between a central unit and a remote unit wherein the need for tracking loops in the central unit has been eliminated. The central unit transmitter generates a master carrier and a master clock ...

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**Rakib, Selim Shlomo / Azenkot, Yehuda, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION**, Nov 2001


...the shared media. In the preferred embodiment, each chip is a QAM modulated element of a result vector where the result vector...having 144 elements. These 144 result vector elements will be QAM modulated to generate the 144 chips that are transmitted as a...

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**Rakib, Selim Shlomo / Azenkot, Yehuda, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT**, Oct 2001

A system for bidirectional communication of digital data between a central unit and a remote unit wherein the need for tracking loops in the central unit has been eliminated. The central unit transmitter generates a master carrier and a master clock ...

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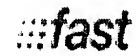
☐ **20. [Apparatus and method for trellis encoding data for transmission in digital data transmission systems](#)**

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A system for bidirectional communication of digital data between a central unit and a remote unit wherein the need for tracking loops in the central unit has been eliminated. The central unit transmitter generates a master carrier and a master clock ...

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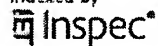
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Solid-State Circuits, IEEE Journal of  
 Volume 39, Issue 4, April 2004 Page(s):643 - 650  
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 Jeng-Liang Tsai; DongHyun Baik; Chen, C.C.-P.; Saluja, K.K.;  
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 delay generation  
 Donghwan Lee; Jinho Sung; Jaehong Park;  
Nuclear Science Symposium Conference Record, 2003 IEEE  
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6. **A 30 MHz DDS clock generator with sub-ns time domain interpolator and spurious level**  
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10. **A digitally programmable delay element: design and analysis**  
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11. **A clock-tuning circuit for system-on-chip**  
Elboim, Y.; Kolodny, A.; Ginosar, R.;  
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13. **A Receiver with Start-up Initialization and Programmable Delays for Wire Distribution**

Xiaoling Guo; Dong-Jun Yang; Ran Li; O., K.K.;

[Solid-State Circuits, 2006 IEEE International Conference Digest of Technical F](#)  
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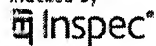
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